



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Shunpei YAMAZAKI et al.

Serial No. 10/587,744

Filed: July 28, 2006

For: METHOD FOR MANUFACTURING

THIN FILM INTEGRATED CIRCUIT,

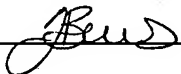
AND ELEMENT SUBSTRATE

) Group Art Unit: 2823

) Examiner: Su Jya Fan

) CERTIFICATE OF MAILING

) I hereby certify that this correspondence is
) being deposited with the United States Postal
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) 

**CORRECTION TO PREVIOUSLY SUBMITTED INFORMATION DISCLOSURE
STATEMENT**


Honorable Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

It has come to applicant's attention that the Information Disclosure Statement filed September 14, 2006 included incomplete bibliographic information. Specifically, the Nikkei Electronics citation has been corrected to include "No. 835".

The above inaccuracy is corrected on the 1449 Form attached hereto. The Examiner is requested to initial the attached PTO Form 1449 evidencing consideration of the above reference and line through this reference on the previously submitted Information Disclosure Statement to ensure that the correct information is printed correctly on any issued patent.

Respectfully submitted,


Eric J. Robinson
Reg. No. 38,285

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